DIGITAL LOGIC DESIGN LAB

Course Code:22EC1108

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Course Outcomes: At the end of the course the student will be able to

CO1: Understand the concepts of digital arithmetic (L2)

CO2: Illustrate the functionality of different digital circuits (L3)

CO3: Analyze various Combinational circuits (L4)

CO4: Analyze different Sequential circuits (L4)

CO5: Demonstrate the simulation and synthesis of different digital circuits (L3)

List of Experiments:

1. Logic gates using universal gate
2. Half adder using Logic gates
3. Full Adder using Half Adder
4. BCD addition
5. 4- bit Parity Generator
6. 4x1 multiplexer
7. 2 to 4 decoder
8. 8 to 3 encoder
9. 4 bit Comparator
10. 1x4 demultiplexer
11. Binary to Gray Code converter
12. SR flip-flops, D flip-flops,T flip-flops
13. 4 bit shift Register
14. 4 bit counter

Note: Any TWELVE **of the experiments are to be conducted**

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