About GVPCE(A)

Gayatri Vidya Parishad College of Engineering (GVPCE)
Established in **1996** with a vision to deliver quality education, GVPCE has grown from four branches with 200 students to a premier institution offering **11 B.Tech., 5 M.Tech., and MCA programs** with an annual intake of **1380 students**, all approved by **AICTE**.

Recognized under **UGC Act 2(f) & 12(B)**, the college was conferred **autonomous status in 2009**, extended till 2035. Accredited by **NAAC with an "A++" grade for seven years in the third cycle**. GVPCE stands among the top institu- tions in India. All eligible B.Tech programs are accredited by the **National Board of Accreditation (NBA)** at least three times.

GVPCE has secured **Rs 5 Crores under TEQIP-II** for postgraduate education, R&D, and innova- tion, and received **Rs 13.5 Crores in funding** from AICTE, DST, NBHM, ARB, and others for **45 R&D projects**.

Honored as the **Best Engineering College of Andhra Pradesh & Telangana** by ISTE, the college fosters innovation through its **Skill Development Centre** (six Siemens-APSSDC labs) and a vibrant **Incubation Centre** hosting 10 start-ups.

With strong academia–industry collaborations, state-of-the-art infrastructure, and a focus on real-world learning, GVPCE continues to inspire excellence and drive innovation in engineering education.

About The Departments

The Departments of **Computer Science and Engineering, CSE (AI & ML), CSE (Data Science), IT, and Computer Applications** at GVPCE together offer **B.Tech., M.Tech. (CSE), and MCA programs** with an annual intake of **612t students**.

A strong curriculum in **AI, ML, Cybersecurity, Data Science, and Image Processing** blends theory with practical learning. With **74 expert faculty** (including **26 Ph.Ds**. and **21 pursuing Ph.Ds**. at IITs, NITs, and other reputed institutes), the departments provide academic rigor and mentorship of high caliber.

Supported by **state-of-the-art infrastruc- ture and MoUs with global leaders like IBM, TCS, CISCO & Quantum AI Global** , students are empowered to excel in evolving technologies and shape the future of innova- tion.

Who Should Attend

Faculty, M.Tech., Research Scholars from all the Departments, and must carry a laptop

NOTE: Participants who Attend 80% sessions and score 70% in the test would get a fee refund.

Registration Link
https://forms.gle/4ehifRDXCK4a77ju6

Registration Fee: Rs 500

Snap this to Pay

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING
(Autonomous)
Approved by AICTE & Affiliated to Andhra University, Visakhapatnam from 2022-23
(Affiliated to JNTUK, Kakinada upto 2021-22)
Accredited by NAAC at 'A++' Grade for 7 years in the 3rd Cycle

One Week Faculty Development Program in Hybrid Mode on Fundamentals of Quantum Computing & Programming

September 17-22, 2025
Full Time: 10 AM - 5 PM
(Including Lunch)

COORDINATORS

Dr. C.V. Guru Rao
Professor & HoD, CSE

Dr. K. B. Madhuri
Professor & Dean, School of CSE, IT & MCA

CO-COORDINATOR
Dr. S. Kanthi Kiran
Contact- 9550331221

Organized by
Departments of
Computer Science and Engineering, CSE(AIML), CSE(DS), IT and Computer Applications

In Collaboration with

QUANTUM AI GLOBAL

OBJECTIVE OF FDP

With industries rapidly embracing **quantum computing** to address complex computational challenges across finance, healthcare, and pharmaceuticals, the need for skilled educators has never been greater. This **Faculty Development Program (FDP)** is designed to equip participants with both **fundamental knowledge and practical skills** in quantum computing.

Through **hands-on training with IBM Quantum Composer and the Qiskit programming environment**, participants will learn to design quantum circuits, implement advanced algorithms such as **Grover's search and quantum cryptography protocols**, and explore real-world applications.

The ultimate goal is to empower faculty members to confidently teach and integrate **Minor and B.Tech programs in Quantum Computing**, thereby preparing the next generation for the quantum era.

Expected Outcomes : By the end of the FDP, participants will:

- Master the fundamentals of quantum circuit design.
- Gain practical exposure to quantum gates and multi-qubit architectures.
- Build confidence to independently design, test, and implement quantum algorithms and applications.

RESOURCE PERSONS

- **Prof. P Venkata Subba Reddy**, NITW.
- **Diksha Sharma** - Quantum Research Scientist, Quantum AI Global
- **Anekait Kariya** - Quantum Research Engineer, Quantum AI Global
- **Gopika Chaganti** – Research and Innovations lead, Quantum AI Global
- **Devender Mishra** – Quantum Hardware Lead, Quantum AI Global
- **Riktheem Bhowmick** – Quantum Cryptography & Communications Lead, Quantum AI Global
- **Ex Col Kapil Jaiswal** – CTO, Quantum AI Global

LIST OF TOPICS

- Introduction to Quantum Computing and National Quantum Mission of India
- Classical Bits and Quantum Bits: Fundamental Differences
- **Quantum Gates**: Theory, Properties and Multi-Qubit Systems
- Visual Quantum Circuit Design Using IBM Quantum Composer
- **Hands-on**: Qiskit Programming Environment
- **Demo**: Quantum Random Number Generation
- **Demo**: Quantum Entanglement and Bell State Preparation, etc...t

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President, GVP

PATRON
Sri D. Dakshina Murthy
Vice-President, GVP

Prof. K. P. R. Sastry
Vice-President, GVP

Prof. P. Soma Raju
Secretary, GVP

CHAIRMAN
Prof. A. B. K. Rao
Principal, GVPCE(A)

VICE CHAIRMAN
Prof. Dr. A. Syamsundar
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Head of the Department of IT

ORGANISING SECRETARY
Dr. Y. Anuradha
Professor & Head, Department of CSE(DS)