

Regulation 2012

G.V.P.COLLEGE OF ENGINEERING (AUTONOMOUS)

B.Tech V-Semester Supplementary (Autonomous) Examinations, October-2020

(For 2012 Admitted Batch only)

| Date | T I M E T A B L E | | | | | | | T I M E : 02.00 P M t o 05.00 P M | | | | | | |
|---------------------------|-------------------|--|---|---|---|-----|------------------------------------|-----------------------------------|-------|-----|-----|-----|-----|------------|
| | Chemical | Civil | CSE | ECE | EEE | IT | Mechanical | Chemical | Civil | CSE | ECE | EEE | IT | Mechanical |
| 16-10-2020 (Friday) | --- | --- | Artificial Intelligence (ACT1131) | Microprocessors and Interfacing (AEC1115) | Linear and Digital IC Applications (AEC1147) | --- | Thermal Engineering-2 (AME1120) | --- | --- | --- | --- | --- | --- | --- |
| 19-10-2020 (Monday) | --- | Geotechnical Engineering - I (ACE1120) | Design and Analysis of Algorithms (ACT1116) | Antennas and Wave Propagation (AEC1116) | Performance and Design of AC Machines-II (AEE1113) | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 21-10-2020 (Wednesday) | --- | --- | Computer Graphics (ACT1113) | VLSI Design (AEC1117) | Power Electronics (AEE1112) | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 27-10-2020 (Tuesday) | --- | --- | Formal Languages and Automata Theory (ACT1114) | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 29-10-2020 (Thursday) | --- | --- | Microprocessors and Interfacing (ACT1115) (Common to CSE&IT) | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |

Note : (i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY.

(ii) FOR ANY OTHER CLARIFICATION IN RESPECT OF THE ABOVE EXAMINATIONS PLEASE CONTACT THE CONTROLLER OF EXAMINATIONS.

Date :

CONTROLLER OF EXAMINATION